1) CUSTOMER INFORMATION

 Company:       Address 1:

 Contact Name:       Address 2:

 Position:       City:

 Phone:       State:

 Fax:       Zip:

 Email:       Country:

2) INSTRUMENT RFQ DETAILS

|  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- |
| **Date of RFQ** | **RFQ #** | **Preferred/Acceptable Delivery Schedule** | **Quantity to Quote** | **Quote Validity** | **Parts Selection** | **Special Instructions** |
| Click here to enter a date. | [ ]  N/A [ ]        | [ ]  ASAP[ ]  12-16 wks ARO[ ]  16-20 wks ARO[ ]  20+ wks ARO  | [ ]  1[ ]  2-4[ ]  5-9[ ]  10-19[ ]  20-49[ ]  50+ | [ ]  60 days[ ]  90 days[ ]  180 days[ ]  Specific Date:  Click here to enter a date. | [ ]  No Preference[ ]  COTS[ ]  COTS+[ ]  HI-REL [ ]  Other       |       |

3) MECHANICAL DETAILS

Each instrument is comprised of a 19” standard rack-mount aluminum enclosure chassis and all necessary low noise components needed to form a complete system. Please select the preferred configuration below for consideration during the final design process:

|  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- |
| **Height** | **Depth** | **Front Panel** | **Monitor****LEDs on Front Panel** | **Monitor****DB Connector** Monitor Signals Provided (Same as LED selection) | **Cooling** | **Weight** | **Mounting** |
| [ ]  No Preference[ ]  1U (1.75”) [ ]  2U (3.5”) [ ]  3U (5.25”) [ ]  5U (8.75”) [ ]  Other       | [ ]  No Preference[ ]  8” max[ ]  17” max[ ]  22” max[ ]  26” max[ ]  Other       | [ ]  Painted White with Black Lettering[ ]  Painted Black with White Lettering[ ]  FED Color Code:      [ ]  Other       | [ ]  No Preference [ ]  Power On [ ]  External Reference Detect[ ]  Phase Lock Detect(s)[ ]  Output Level Detect(s) [ ]  Rail Detect(s) [ ]  Oven Monitor Detect(s)[ ]  Global Alarm[ ]  Other       | [ ]  Not Required [ ]  9-pin [ ]  15-pin [ ]  25-pin[ ]  TTL(5V)[ ]  LVTTL(2.5V) [ ]  CMOS(3.3V) [ ]  Other       | [ ]  No Preference[ ]  Include fan(s)[ ]  Include vent holes (front panel)[ ]  Include vent holes (floor & lid)  | [ ]  No Preference[ ]  < 25 lbs., goal[ ]  < 35 lbs., goal [ ]  < 50 lbs., goal [ ]  Other       | [ ]  Front panel holes & rear rack support bracket\*[ ]  Provisions for mounting slides; customer provides slides[ ]  Wenzel provides specified slides with instrument[ ]  Slide Details:       |

\* It is not recommended to support instruments weighing >5 lbs. using front panel mounting holes alone. A customer-supplied bracket is suggested to support the weight at the rear of the instrument.

4) SUPPLY VOLTAGE 5) TEST DATA PROVIDED

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| **AC Supply** | **DC Supply** | **Maximum Current** |  | **Standard Electrical Tests** | **Other Testing Required** |
| [ ]  N/A [ ]  115 VAC ±10%, 50/60 Hz [ ]  230 VAC ±10%, 50/60 Hz [ ]  Other       | [ ]  N/A [ ]  +15 VDC ±5% [ ]  +18 VDC ±5% [ ]  +28 VDC ±5% [ ]  +48 VDC ±5% [ ]  Other       | [ ]  No Preference [ ]  2 Amps [ ]  3 Amps [ ]  4 Amps [ ]  5 Amps[ ]  Other      |  | [ ]  N/A [ ]  Output Level(s) [ ]  Port-to-Port Isolation [ ]  Phase Noise L(f), Static [ ]  Other        | [ ]  Harmonics [ ]  Sub-Harmonics [ ]  Reference PLL Products[ ]  Spurious [ ]  Other       | [ ]  N/A [ ]       [ ]       [ ]       [ ]       [ ]        |

6) ENVIRONMENT

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
|  | **Operating Temperature Range** | **Storage Temperature Range** | **MTBF Prediction** | **Other Environmental Conditions** | **MILITARY Specifications** **(Standards, Methods, Procedures, etc.)** |
|  | **MTBF, goal** | **End Use Environment** | **Env****Temp** | **Duty Cycle** |
|  | [ ]  No Preference[ ]  +25 ±10°C[ ]  0 to +50°C[ ]  -20 to +70°C [ ]  Other       | [ ]  No Preference[ ]  -20 to +70°C[ ]  -40 to +85°C[ ]  -55 to +90°C [ ]  Other       | [ ]  N/A[ ]  ≥ 20k Hrs[ ]  ≥ 50k Hrs[ ]  ≥ 100k Hrs[ ]  Other      | [ ]  GB [ ]  AIC [ ]  GF [ ]  AIF [ ]  GM [ ]  AUC [ ]  NS [ ]  AUF[ ]  NU [ ]  ARW[ ]  Other        | [ ]  +25°C[ ]  +30°C[ ]  +40°C[ ]  +50°C[ ]  Other      | [ ]  25%[ ]  50%[ ]  100%[ ]  Other      | [ ]  N/A[ ]       [ ]       [ ]       [ ]       [ ]         | [ ]  N/A[ ]       [ ]       [ ]       [ ]        [ ]         |

7) INPUT SPECIFICATIONS

|  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Frequency** (MHz) | **Signal Type** | **Input Level** (Sine, into 50 ohms, dBm) | **Frequency Accuracy** | **Connector Type** | **Connector Location** | **Phase Noise L(f)** **dBc/Hz** | **Harmonics** | **Sub-Harmonics** | **Spurious**  |
| [ ]  N/A [ ]  5 MHz [ ]  10 MHz [ ]  Other       | [ ]  Sine [ ]  TTL (5V) [ ]  LVTTL (2.5V) [ ]  CMOS (3.3V) [ ]  Other      | [ ]  N/A [ ]  0 to +15 [ ]  0 ±3 [ ]  +10 ±2 [ ]  +13 ±2[ ]  +7 ±6[ ]  Other      | [ ]  ≤ ±2E-9[ ]  ≤ ±5E-8[ ]  ≤ ±5E-7[ ]  ≤ ±2E-6 [ ]  Other      | [ ]  SMA(f)[ ]  BNC(f) [ ]  TNC(f) [ ]  N-Type(f)[ ]  Other      | [ ]  Front Panel [ ]  Rear Panel | [ ]  Unknown [ ]  from Wenzel P/N:     [ ]  from other source:  | [ ]  Unknown[ ]  -20 dBc [ ]  -30 dBc[ ]  -40 dBc[ ]  -50 dBc [ ]  -80 dBc[ ]  Other      | [ ]  Unknown[ ]  N/A[ ]  -40 dBc [ ]  -50 dBc[ ]  -60 dBc[ ]  -80 dBc[ ]  Other      | [ ]  Unknown[ ]  -70 dBc[ ]  -80 dBc[ ]  -100 dBc[ ]  Other      |
| 1Hz: |      | 10kHz: |      |
| 10Hz: |      | 100kHz: |      |
| 100Hz: |      | 1MHz: |      |
| 1kHz: |      | 10MHz: |      |

8) OUTPUT SPECIFICATIONS

|  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
|  | **Frequency** (MHz) | **Signal Type** | **Output Level** (Sine, into 50 ohms, each output, dBm) | **No. of Outputs** | **Port-to-Port Isolation** | **Connector Type** | **Connector Location** | **Phase Noise L(f)** **dBc/Hz** | **Harmonics** | **Sub-Harmonics** | **Ref PLL Products** (when phase locked) | **Spurious** (excluding power supply line spurs) |
| **Output A** |       | [ ]  Sine [ ]  TTL (5V) [ ]  LVTTL (2.5V) [ ]  LVDS [ ]  Other      | [ ]  N/A [ ]  +10 ±2 [ ]  +13 ±2 [ ]  +16 ±2 [ ]  +20 ±2 [ ]  Other      | [ ]  1 [ ]  Other      | [ ]  N/A [ ]  ≥ 20 dB[ ]  Other      | [ ]  SMA(f)[ ]  BNC(f) [ ]  TNC(f) [ ]  N-Type(f)[ ]  Other      | [ ]  Front [x]  Rear  | [ ]  Standard (Good, $)[ ]  ULN (Better, $$)[ ]  Golden (Best, $$$)[ ]  Specify Goal: | [ ]  -20 dBc [ ]  -30 dBc[ ]  -40 dBc[ ]  -50 dBc [ ]  -80 dBc[ ]  Other      | [ ]  N/A[ ]  -40 dBc[ ]  -50 dBc[ ]  -60 dBc[ ]  -80 dBc[ ]  Other      | [ ]  N/A[ ]  -50 dBc[ ]  -60 dBc[ ]  -80 dBc[ ]  Other      | [ ]  -70 dBc[ ]  -80 dBc[ ]  -100 dBc[ ]  Other      |
| 1Hz: |      | 10kHz: |      |
| 10Hz: |      | 100kHz: |      |
| 100Hz: |      | 1MHz: |      |
| 1kHz: |      | 10MHz: |      |
| **Output B** |       | [ ]  Sine [ ]  TTL (5V) [ ]  LVTTL (2.5V) [ ]  LVDS [ ]  Other       | [ ]  N/A [ ]  +10 ±2 [ ]  +13 ±2 [ ]  +16 ±2 [ ]  +20 ±2 [ ]  Other      | [ ]  1 [ ]  Other      | [ ]  N/A [ ]  ≥ 20 dB[ ]  Other      | [ ]  SMA(f)[ ]  BNC(f) [ ]  TNC(f) [ ]  N-Type(f)[ ]  Other      | [ ]  Front [ ]  Rear  | [ ]  Standard (Good, $)[ ]  ULN (Better, $$)[ ]  Golden (Best, $$$)[ ]  Specify Goal: | [ ]  -20 dBc [ ]  -30 dBc[ ]  -40 dBc[ ]  -50 dBc [ ]  -80 dBc[ ]  Other      | [ ]  N/A[ ]  -40 dBc[ ]  -50 dBc[ ]  -60 dBc[ ]  -80 dBc[ ]  Other      | [ ]  N/A[ ]  -50 dBc[ ]  -60 dBc[ ]  -80 dBc[ ]  Other      | [ ]  -70 dBc[ ]  -80 dBc[ ]  -100 dBc[ ]  Other      |
| 1Hz: |      | 10kHz: |      |
| 10Hz: |      | 100kHz: |      |
| 100Hz: |      | 1MHz: |      |
| 1kHz: |      | 10MHz: |      |
| **Output C** |       | [ ]  Sine [ ]  TTL (5V) [ ]  LVTTL (2.5V) [ ]  LVDS [ ]  Other      | [ ]  N/A [ ]  +10 ±2 [ ]  +13 ±2 [ ]  +16 ±2 [ ]  +20 ±2 [ ]  Other      | [ ]  1 [ ]  Other      | [ ]  N/A [ ]  ≥ 20 dB[ ]  Other      | [ ]  SMA(f)[ ]  BNC(f) [ ]  TNC(f) [ ]  N-Type(f)[ ]  Other      | [ ]  Front [ ]  Rear  | [ ]  Standard (Good, $)[ ]  ULN (Better, $$)[ ]  Golden (Best, $$$)[ ]  Specify Goal: | [ ]  -20 dBc [ ]  -30 dBc[ ]  -40 dBc[ ]  -50 dBc [ ]  -80 dBc[ ]  Other      | [ ]  N/A[ ]  -40 dBc[ ]  -50 dBc[ ]  -60 dBc[ ]  -80 dBc[ ]  Other      | [ ]  N/A[ ]  -50 dBc[ ]  -60 dBc[ ]  -80 dBc[ ]  Other      | [ ]  -70 dBc[ ]  -80 dBc[ ]  -100 dBc[ ]  Other      |
| 1Hz: |      | 10kHz: |      |
| 10Hz: |      | 100kHz: |      |
| 100Hz: |      | 1MHz: |      |
| 1kHz: |      | 10MHz: |      |
| **Output D** |       | [ ]  Sine [ ]  TTL (5V) [ ]  LVTTL (2.5V) [ ]  LVDS [ ]  Other      | [ ]  N/A [ ]  +10 ±2 [ ]  +13 ±2 [ ]  +16 ±2 [ ]  +20 ±2 [ ]  Other      | [ ]  1 [ ]  Other      | [ ]  N/A [ ]  ≥ 20 dB[ ]  Other      | [ ]  SMA(f)[ ]  BNC(f) [ ]  TNC(f) [ ]  N-Type(f)[ ]  Other      | [ ]  Front [ ]  Rear  | [ ]  Standard (Good, $)[ ]  ULN (Better, $$)[ ]  Golden (Best, $$$)[ ]  Specify Goal: | [ ]  -20 dBc [ ]  -30 dBc[ ]  -40 dBc[ ]  -50 dBc [ ]  -80 dBc[ ]  Other      | [ ]  N/A[ ]  -40 dBc[ ]  -50 dBc[ ]  -60 dBc[ ]  -80 dBc[ ]  Other      | [ ]  N/A[ ]  -50 dBc[ ]  -60 dBc[ ]  -80 dBc[ ]  Other      | [ ]  -70 dBc[ ]  -80 dBc[ ]  -100 dBc[ ]  Other      |
| 1Hz: |      | 10kHz: |      |
| 10Hz: |      | 100kHz: |      |
| 100Hz: |      | 1MHz: |      |
| 1kHz: |      | 10MHz: |      |
| **Output E** |       | [ ]  Sine [ ]  TTL (5V) [ ]  LVTTL (2.5V) [ ]  LVDS [ ]  Other      | [ ]  N/A [ ]  +10 ±2 [ ]  +13 ±2 [ ]  +16 ±2 [ ]  +20 ±2 [ ]  Other      | [ ]  1 [ ]  Other      | [ ]  N/A [ ]  ≥ 20 dB[ ]  Other      | [ ]  SMA(f)[ ]  BNC(f) [ ]  TNC(f) [ ]  N-Type(f)[ ]  Other      | [ ]  Front [ ]  Rear  | [ ]  Standard (Good, $)[ ]  ULN (Better, $$)[ ]  Golden (Best, $$$)[ ]  Specify Goal: | [ ]  -20 dBc [ ]  -30 dBc[ ]  -40 dBc[ ]  -50 dBc [ ]  -80 dBc[ ]  Other      | [ ]  N/A[ ]  -40 dBc[ ]  -50 dBc[ ]  -60 dBc[ ]  -80 dBc[ ]  Other      | [ ]  N/A[ ]  -50 dBc[ ]  -60 dBc[ ]  -80 dBc[ ]  Other      | [ ]  -70 dBc[ ]  -80 dBc[ ]  -100 dBc[ ]  Other      |
| 1Hz: |      | 10kHz: |      |
| 10Hz: |      | 100kHz: |      |
| 100Hz: |      | 1MHz: |      |