

REV	DATE	REVISION RECORD	DWN	AUTH
-	02-16-05	Draft	Liz	Liz

**INPUT**

**Frequency**

10 MHz,  $\pm 2 \times 10^{-6}$

**Level**

+7 dBm  $\pm 5$  dB into 50 ohms

**OUTPUT**

**Frequency**

80 MHz

**Level**

+13 dBm  $\pm 2$  dB into 50 ohms

**STABILITY**

**Output Phase Noise L(f)**

**(Free-Running)**

10 Hz -94 dBc \*

100 Hz -124 dBc

1 kHz -152 dBc

10 kHz -165 dBc

\*10 Hz, typical, not measured

**Aging**

$\pm 1 \times 10^{-6}$  per year after 30 days operating, typical

**Temperature Stability**

$\pm 5 \times 10^{-7}$  free-running from 0 to +50°C, (Ref. +25°C)

**Harmonics**

-30 dBc

**Sub-Harmonics and Products**

-50 dBc

**Non-Harmonic Spurious, typical**

-80 dBc

**Phase Lock Alarm**

TTL

Locked: +3.5 VDC to +5.2 VDC (Hi)

Out-of-Lock: +0.8 VDC max (Lo)

**Phase Lock Voltage Monitor**

Voltage monitor pin supplied

**MECHANICAL**

**Dimensions**

2.5 x 3.5 x 0.8"

**Connectors**

SMA's and solder pins on side  
Feed-thru terminals for lock alarm, supply and phase lock voltage monitor

**Packaging**

Machined aluminum housing

**Mounting**

Tapped holes on sides, 16 places

Through holes, 4 places

Threaded inserts on base, 4 places

**POWER REQUIREMENTS**

**Supply Voltage**

+15 VDC

**Warm-Up Power**

8 Watts at start-up for 5 minutes at +25°C

**Total Power**

5 Watts at steady state +25°C

**ADJUSTMENT**

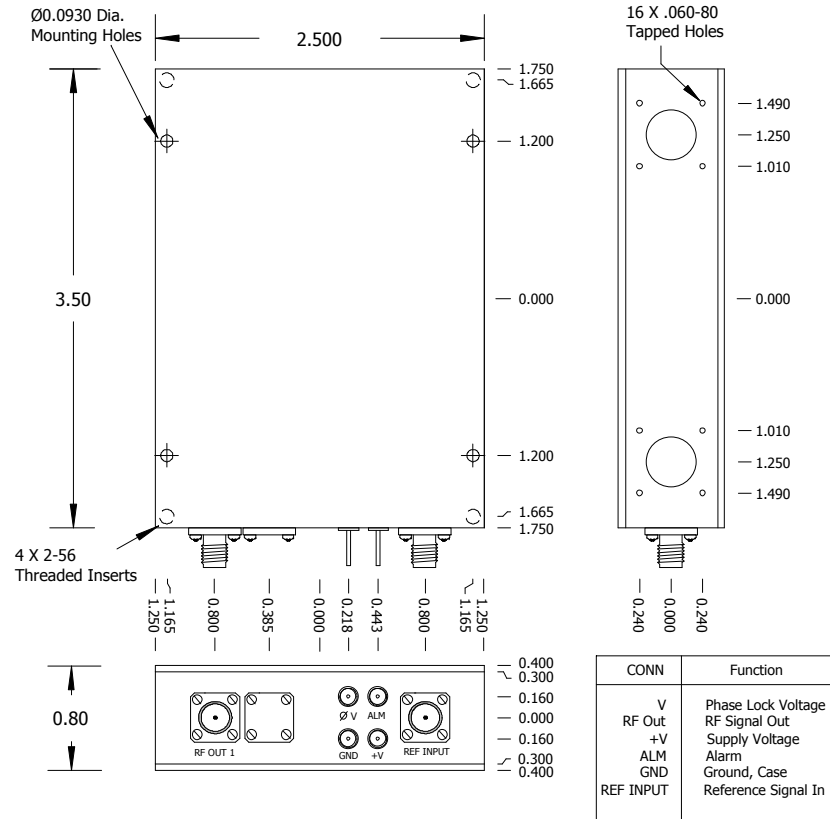
**Loop BW**


Target Bandwidth: <10 Hz  
Type 2 Loop

**CRYSTAL**

**Type**

SC-cut



 <b>Wenzel Associates, Inc.</b> Austin, Texas				
Title:				
<b>80 MHz-SC Phase Lock Crystal Oscillator</b>				
P/N:	Rev:	Date:	Drawn:	Ref:
<b>501-14057</b>	-	<b>02-16-05</b>		
ces: (except as noted) Dimensions are in inches	0.XX Dec: <b>±0.030"</b>	0.XXX Dec: <b>±0.010"</b>	FSCM: <b>62821</b>	Page 1 of 1