

**OUTPUT****Frequency**

500 MHz, dual

The outputs will be within 5° of phase

**Level**+10 dBm ±2 dB into 50 ohms,  
each output**INPUT****Frequency**

10 MHz

**Level**

+7 dBm ±5 dB into 50 ohms

**STABILITY****Aging**1 x 10<sup>-6</sup> per year after 30 days operating,  
typical**Phase Noise L(f), free running**

100 Hz -108 dBc/Hz

1 KHz -138 dBc/Hz

10 KHz -158 dBc/Hz

100 kHz -158 dBc/Hz

**Temperature Stability**±5 x 10<sup>-7</sup>, 0° to +50°C (Ref +25°C)**Sub-Harmonics and Related Products**

-50 dBc

**Harmonics**

-30 dBc

**MECHANICAL****Dimensions**

5 x 6 x 2.62"

**Connectors**

SMA(f) and feedthru capacitors

**Packaging**Aluminum housing with extended  
mounting cover and gasketed  
access screw**POWER REQUIREMENTS****Warm-Up Power**

≤ 20 Watts max for &lt; 5 minutes

**Total Power**

≤ 15 Watts at +25°C

**Supply Voltage**

+15 VDC ±5%

**ADJUSTMENT****Loop BW**

≤ 10 Hz

Type 2 Loop

**CRYSTAL****Type**

SC-cut

**OTHER****Marking**

Label connectors

**PLL Voltage Out**

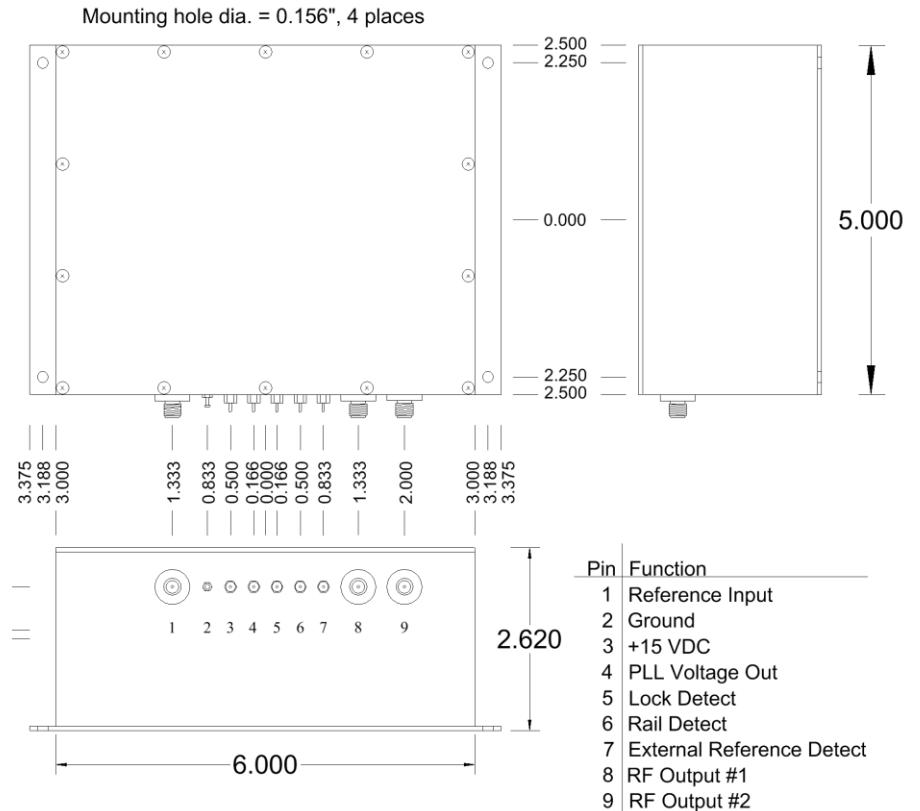
0 to +10 VDC output of PLL

**Lock Detect**

TTL high when locked

**Rail Detect**TTL high when PLL is  
within 0.5 VDC of losing lock**External Reference Detect**TTL high when external  
reference is present

REV	DATE	REVISION RECORD	DWN	AUTH
-	03-04-14	Initial Release	PAC	

**Wenzel Associates, Inc.**

Austin, Texas

Title:

**500 MHz-SC Sorcerer Phase Locked Source**

P/N:

**501-12778**

Rev:

-

Date:

**03-04-14**

Drawn:

Ref:

10985

Tolerances:  
(except as noted)  
Dimensions are in inches

0.XX Dec:

±0.030"

0.XXX Dec:

±0.010"

FSCM:

62821

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