

INPUT**Frequency**10 MHz, $\pm 1 \times 10^{-6}$ **Level**

+0 dBm into 50 Ohms

Input Phase Noise

10 Hz	-140 dBc
100 Hz	-160 dBc
1 kHz	-169 dBc
10 kHz	-171 dBc
100 kHz	-171 dBc
1 MHz	-171 dBc

OUTPUTS**Frequency**

10 MHz, 8 outputs

Level

+10 dBm minimum out of 8 ports, 50 ohms each, when input is present

Frequency

100 MHz, 8 outputs

Level

+10 dBm minimum out of 8 ports, 50 ohms each

STABILITY**Output Phase Noise L(f)****Free-running**

	10 MHz	100 MHz
10 Hz	N/A	-100 dBc
100 Hz	N/A	-130 dBc
1 kHz	N/A	-155 dBc
10 kHz	N/A	-173 dBc
100 kHz	N/A	-174 dBc
1 MHz	N/A	-175 dBc

Locked

	10 MHz	100 MHz
10 Hz	-140 dBc	-115 dBc
100 Hz	-160 dBc	-135 dBc
1 kHz	-169 dBc	-155 dBc
10 kHz	-171 dBc	-173 dBc
100 kHz	-171 dBc	-174 dBc
1 MHz	-171 dBc	-175 dBc

Aging $\pm 1 \times 10^{-6}$ per year after 90 days operating, typical**Temperature Stability** $\pm 2.5 \times 10^{-7}$ free-running from 0 to +50°C, (Ref. +25°C)**Harmonics**

-30 dBc

Sub-Harmonics (10 MHz)

-130 dBc

Spurious (Omit 60 Hz)

-130 dBc

MECHANICAL**Dimensions**

8 x 8 x 1.25" maximum

Connectors

SMA's and solder pins on side

Packaging

Blue Tops and ULN mounted on a machined aluminum plate

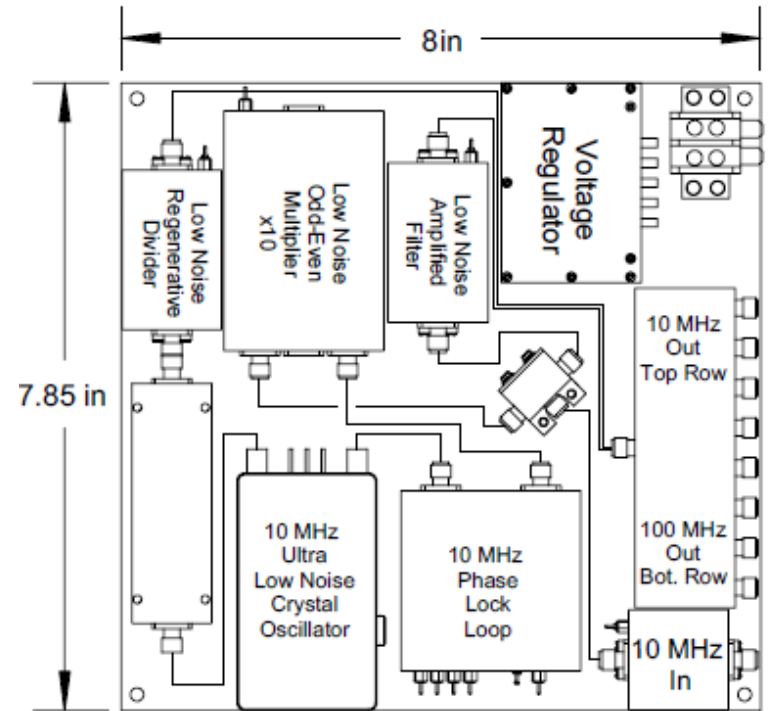
Mounting

Through holes, 4 places

POWER REQUIREMENTS**Supply Voltage**+24 VDC, 880 mA warm-up
750 mA steady state @ +25°C**ADJUSTMENT****Loop BW**Target Bandwidth: 300 Hz
Type 2 Loop**Electrical Tuning (not locked)**

+2.0, -5.0 ppm

REV	DATE	REVISION RECORD	DWN	AUTH
-	04-26-11	Draft	Liz	

**Wenzel Associates, Inc.**

Austin, Texas

Title:

10 and 100 MHz Phase Locked Ultra Low Noise Plate

P/N:

501-17387

Rev:

-

Date:

04-26-11

Drawn:

Ref:

Tolerances:
(except as noted)
Dimensions are in inches

0.XX Dec:

 ± 0.030 "

0.XXX Dec:

 ± 0.010 "

FSCM:

62821

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